AMENDMENTS TO THE CLAIMS WITH MARKINGS TO SHOW CHANGES MADE, AND LISTING OF ALL CLAIMS WITH PROPER INDENTIFIERS

1. (Currently amended) An [[N]] n-point-converter circuit, comprising:

two converter valves for each converter phase electrically connected in series at corresponding valve connection points, each converter valve having (n-1) turn-off semiconductor switches ~~connected in series~~;

a voltage intermediate circuit having (n-1) capacitors electrically connected in series at corresponding capacitor connection points, each capacitor connection point defining a corresponding DC potential; and

(n-2) cross arms, each cross arm having ~~at least (n-3) anti-serially connected turn-off~~ (n-1) semiconductor switches, of which at least two are anti-serially connected,

wherein free ends of the series-connected converter valves form DC-side terminals, said DC-side terminals connected electrically in parallel with the voltage intermediate circuit, and

wherein the (n-2) cross arms connect ~~at least one~~ the valve connection point of each series-connected pair of the converter valves ~~is connected to at least~~ with a corresponding one of the intermediate potentials of the voltage intermediate circuit capacitor connection points ~~by way of the (n-2) cross arms~~.

2. (Original) The circuit of claim 1, wherein the turn-off semiconductor switches of each converter valve and of each cross arm are arranged side-by-side in a corresponding compression assembly in such a way that the valve connection points and the capacitor connection points are each located on a different side of the compression assembly.

3. (Canceled).

4. (Canceled).

5. (Original) The circuit of claim 1, wherein the turn-off semiconductor switches comprise Insulated Gate Bipolar Transistors (IGBT).

6. (New) An n-point-converter circuit, comprising:

two converter valves for each converter phase electrically connected in series and having free ends forming DC-side terminals, each converter valve having (n-1) turn-off semiconductor switches and a valve connection point;

a voltage intermediate circuit having (n-1) capacitors electrically connected in series at (n-2) corresponding capacitor connection points and connected electrically in parallel with the DC-side terminals, each capacitor connection point defining a corresponding DC potential; and

(n-2) cross arms having turn-off cross arm semiconductor switches and

connecting one of the (n-2) capacitor connection points with a corresponding one of the valve connection points,

wherein a number of the turn-off cross arm semiconductor switches in a cross arm is selected so that each cross arm that connects a valve connection point with a corresponding intermediate potential of the voltage intermediate circuit capacitor connection points has an identical number of turn-off cross arm semiconductor switches, and at least two of the turn-off cross arm semiconductor switches in a cross arm are anti-serially connected.

7. (New) The circuit of claim 6, wherein the turn-off semiconductor switches of each converter valve and the turn-off semiconductor switches of each cross arm are arranged side-by-side in a corresponding compression assembly in such a way that the valve connection points and the capacitor connection points are each located on a different side of the compression assembly.

8. (New) The circuit of claim 6, wherein the turn-off semiconductor switches comprise Insulated Gate Bipolar Transistors (IGBT).